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(snmp or (management <near 1=""></near>	
<pre>protocol*)) <and> trap* <and></and></and></pre>	Γ
(fault* <or> fail*)</or>	1
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- 3) Limit the results by selecting Search Options.
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(snmp or (management <near/1> protocol*)) <and> ((fault* <or> fail*) <sentence> (link* <or> trunk*))

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1 Local sliding control for damping interarea power oscillations

Pourboghrat, F.; Farid, F.; Hatziadoniu, C.J.; Daneshdoost, M.; Mehdian, F.; Lotfalian, M.;

Power Systems, IEEE Transactions on , Volume: 19 , Issue: 2 , May 2004

Pages:1123 - 1134

[Abstract] [PDF Full-Text (616 KB)] **IEEE JNL**

2 Static var compensators (SVC) required to solve the problem of delayed voltage recovery following faults in the power system of the Saudi **Electricity Company, Western Region (SEC-WR)**

Hamzah, I.A.; Yasin, J.A.;

Power Tech Conference Proceedings, 2003 IEEE Bologna, Volume: 4, June 23-26, 2003

Pages:487 - 494

[Abstract]

[Abstract] [PDF Full-Text (485 KB)] **IEEE CNF**

3 Static var compensators (SVC) required to solve the problem of delayed voltaite recovery following faults in the power system of the Saudi **Electricity Company, Western Region (SEC-WR)**

Hamzah, I.A.; Yasin, J.A.;

Quality and Security of Electric Power Delivery Systems, 2003. CIGRE/PES 2003. CIGRE/IEEE PES International Symposium, 8-10 Oct. 2003 Pages:97 - 104

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[PDF Full-Text (463 KB)]

4 Support vector classification for fault diagnostics of an electrical machine

Poyhonen, S.; Negrea, M.; Arkkio, A.; Hyotyniemi, H.; Koivo, H.; Signal Processing, 2002 6th International Conference on , Volume: 2 , 26-30 Aug.

2002 Pages:1719 - 1722 vol.2

[Abstract] [PDF Full-Text (342 KB)] IEEE CNF Membership Publications/Services Standards Conferences Careers/Jobs





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1 Effects of interface trap generation and annihilation on the data retention characteristics of flash memory cells

Jae-Duk Lee; Jeong-Hyuk Choi; Donggun Park; Kinam Kim; Device and Materials Reliability, IEEE Transactions on , Volume: 4 , Issue: 1, March 2004

Pages:110 - 117

[Abstract] [PDF Full-Text (376 KB)] **IEEE JNL**

2 Failure protection in layered networks with shared risk link groups

Dahai Xu; Yizhi Xiong; Chunming Qiao; Guangzhi Li; Network, IEEE, Volume: 18, Issue: 3, May-June 2004

Pages:36 - 41

[Abstract] [PDF Full-Text (1651 KB)] **IEEE JNL**

3 Large domains of continuous grain silicon on glass substrate for highperformance TFTs

Mizuki, T.; Matsuda, J.S.; Nakamura, Y.; Takagi, J.; Yoshida, T.; Electron Devices, IEEE Transactions on , Volume: 51 , Issue: 2 , Feb. 2004 Pages: 204 - 211

[Abstract] [PDF Full-Text (896 KB)] **IEEE JNL**

4 Data retention characteristics of sub-100 nm NAND flash memory cells

Jae-Duk Lee; Jeong-Hyuk Choi; Donggun Park; Kinam Kim; Electron Device Letters, IEEE, Volume: 24, Issue: 12, Dec. 2003 Pages:748 - 750

[Abstract] [PDF Full-Text (256 KB)] **IEEE JNL**

5 Interface delamination in plastic IC packages induced by thermal loading and vapor pressure - a micromechanics model

Liu, P.; Cheng, L.; Zhang, Y.-W.;

Advanced Packaging, IEEE Transactions on [see also Components, Packaging and Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on], Volume: 26, Issue: 1, Feb. 2003

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> [Abstract] [PDF Full-Text (762 KB)] **IEEE CNF**

Pages:129 - 134 vol.1

5 Implementing a highly scalable and adaptive agent-based management framework

Gavalas, D.; Greenwood, D.; Ghanbari, M.; O'Mahony, M.; Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE, Volume: